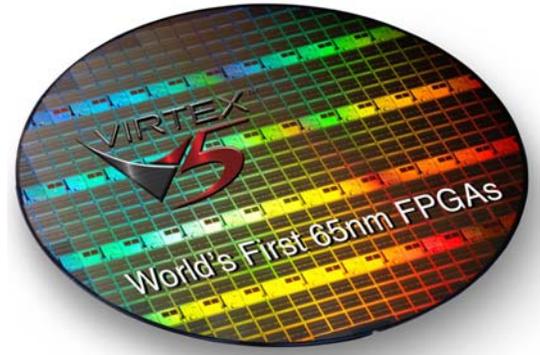


FPGA Solutions

Tekmicro builds products based on four core architectural tenets:

1. The selective utilization of the latest technologies - when a technology supports the design and implementation of an efficient, high performance processing system.
2. The implementation of widely adopted industry standards - to provide optimum interoperability, and ease of upgrade and migration.
3. Architectural consistency across form factors - to support ease of deployment.
4. Full support of rugged deployment - conduction and convection cooling is built into the initial product designs.



Over more than 25 years Tekmicro has worked in collaboration with leading companies in military, medical, and telecommunications -- with engineers involved in the most advanced signal/sensor programs -- to co-create solutions to their demanding requirements.

Tekmicro products include advanced ADC/DAC boards, utilizing the latest, most efficient technologies, systems and solutions configured for advanced data recording, and PMC/XMC cards for specialized applications.

These products are used in real-time systems designed for data acquisition, instrumentation, control systems and signal processing in customer applications such as reconnaissance, signals intelligence, satellite telemetry, mine detection, medical imaging, radar, sonar, semiconductor inspections and seismic research.

Designers of high performance embedded computing systems are constantly pushing the state of the art to fit more processing power into the same box (power, volume, weight) as last year's model. One technology that offers the promise of significant improvements in power and volume efficiency is the use of FPGA technology to replace traditional digital signal processors (DSPs) and general purpose processors (GPPs). The combination of FPGA processing and switch fabric interconnect is a potent one – offering the systems designer dramatic increases in both processing throughput and scalable bandwidth within existing volume and power constraints.

Article links:

[FPGAs Edge Out GPPs for Advanced Signal Processing Apps](#)

[FPGAs and VITA 41 address military sensor processing design challenges](#)

[Design Great Interconnects By Treating FPGAs Like Software](#)

[Modular FPGA architectures create true alternative to DSPs](#)